

**INFORMATION DISCLOSURE
CITATION**

ATTY DOCKET NO.

SERIAL NO.

550-318
APPLICANT

10/079,811

NIGHTINGALE et al

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(Use several sheets if necessary)

February 22, 2002

U.S. PATENT DOCUMENTS

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MAY 06 2002

Technology Center 2100

FOREIGN PATENT DOCUMENTS

TRANSLATION

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

⑧ Xiao-Dong et al, "Implementation of Co-Verification Environment for Embedded System" *Journal of Computer Aided Design and Computer Graphics*, Vol. 12, No. 10, Oct. 2000, pages 736-739

⑧ D. Nadamuni, "Co-Verification Tools: A Market Focus" *Embedded Systems Programming*, Vol. 12, No. 9, Sept. 1999, pages 119-122

⑧ www.mentor.com "Core Based SoC Design System Verification Challenges" 1999

⑧ "Architecture for a Distributed Computing Environment Test Application - Harmonic" *IBM Technical Disclosure Bulletin*, Vol. 39, No. 06, June 1996, pages 259-261

⑧ A. Ghosh et al, "A Hardware-Software Co-Simulator for Embedded System Design and Debugging" *IFIP International Conference on Computer Hardware Description Languages and their Applications*, Aug/Sep 1995, pages 155-164

Examiner Bramley, Anna Date 8/26/05 Date Considered 8/26/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

A circular library stamp. At the top, it says 'OPIE'. In the center, it says 'FEB 16 2007'. Around the bottom edge, it says 'PARENTS PAPERBACKS'. To the right of the date, it says '14253' and '(US)'.

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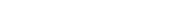
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U.S. PATENT DOCUMENTS

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Date Considered

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